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**BOX AF**

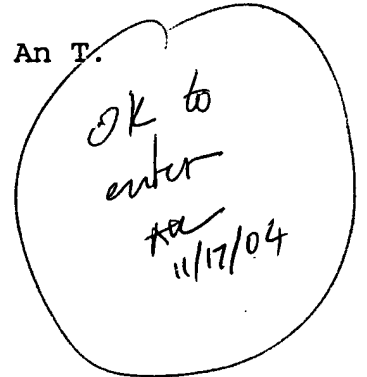
MS AF  
REPLY UNDER  
37 C.F.R. § 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2816

PATENT  
2936-0179P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Okuno, Tomohisa et al. Conf.: 9987  
Appl. No.: 10/602,708 Group: 2816  
Filed: June 25, 2003 Examiner: Luu, An T.  
For: VOLTAGE CONVERSION CIRCUIT AND  
SEMICONDUCTOR INTEGRATED

REPLY AFTER FINAL UNDER 37 C.F.R. § 1.116



MS AF

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

November 8, 2004

Sir:

In reply to the Office Action dated September 8, 2004, the following remarks are respectfully submitted in connection with the above-identified application.

This reply includes: Remarks.